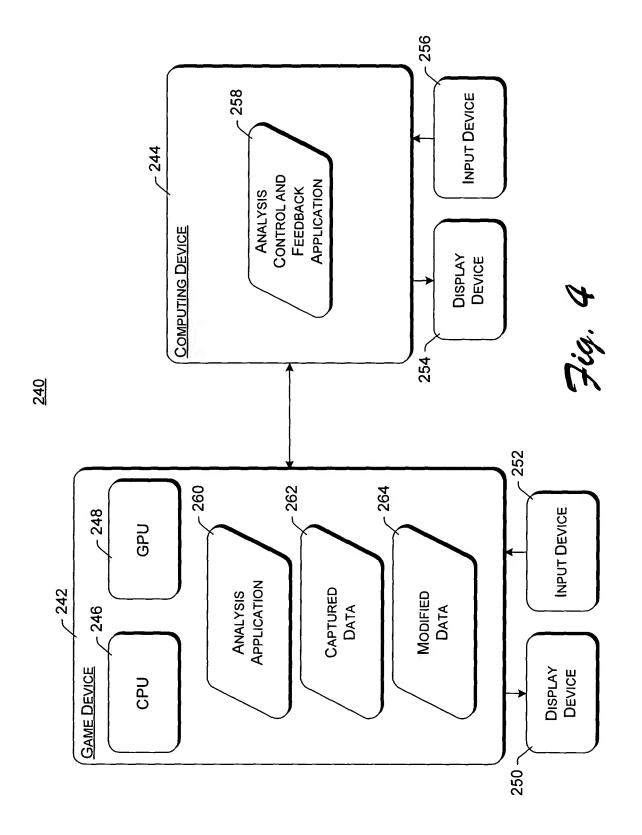


Fig. 3



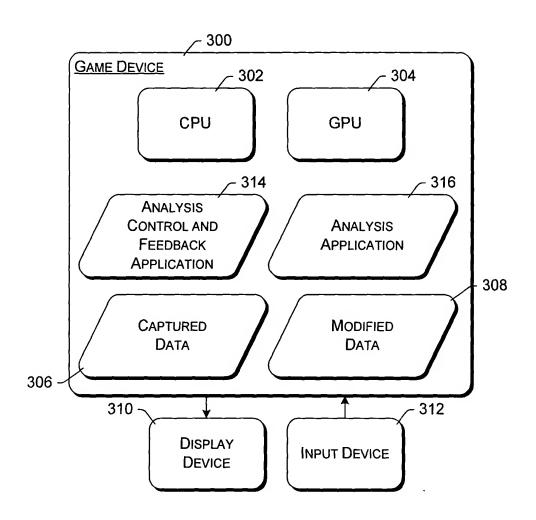


Fig. 5

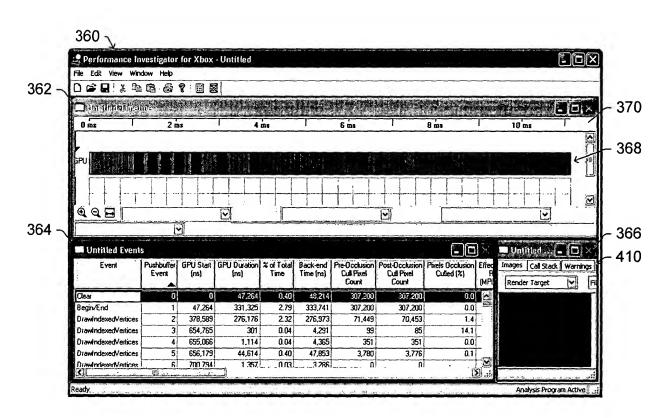


Fig. 7

Timeline Window

342

Events Window

344

Frame Window

346

Fig. 6

380 🔍

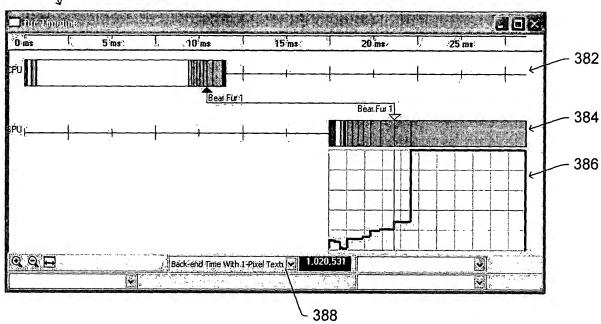


Fig. 8

400 _{\(\sqrt{}\)}

Event	ID	CPU Start (ns)	CPU Duration (ns)	GPU Start (ns)	GPU Duration (ns)	∞ of Total Time	Back end Time (ns)	Selu Time	ID Ins
KickPushBuffer	0	0	14,449	•	The same of the sa		- 1	-	1
-FrameMove	1	36,612	101,750		0			•	1
Clear	3	144,537	4,698	15,745,863	48,640	•	-	-	
Begin/End	4	181,781	29,929	15,794,503	331,584	-	•	-	
∄ Bear Mesh 0									
DrawIndexedVertices	6	252,563	125,782	16,126,087	278,176	•	-	-	
KickPushBuffer	7	339,091	5,501	-	-	•	-		
KickPushBuffer	8	374,790	3,385	•	•	-	-	-	
DrawIndexedVertices	9	386,209	10,399	16,404,263	3,072	-	-	-	
DrawIndexedVertices	10	401,332	6,393	16,407,335	2,656	•	•		10
∄-Bear Mesh 1	11	409,555	56,960	16,409,991	45,568	•	•	-	
Bear Mesh 2	15	466,773	39,522	16,455,559	74,208	-	•	-	
Bear Mesh 3	19	506,536	91,996	16,529,767	59,072	•	•	-	
∄-Bear Mesh 4	25	598,778	53,437	16,588,839	47,232	•	-	-	1
Ð-Bear Mesh 5	29	652,769	39,348	16,636,071	47,552	-	-	-	1
Bear Mesh 6	33	692,356	37,207	16,683,623	45,248	-	-	-	1
⊕Bear Mesh 7	37	729,799	92,051	16,728,871	50,783	•		-	-
Bear Fur 7									Ì
- DrawFins	44	852,610	122,595	16,779,656	156,932	•	•	-	1
⊕ -DrawShells	73	975,455	40,536	16,936,616	61,407	•			7
Bear Fur 6	75	1,019,798	117,933	16,998,024	219,011	-	-	•	-
Bear Fur 5	107	1,138,001	7,341,552	17,217,064	224,739	-	-	-	-
Bear Fur 4	142	8,479,990	164,020	17,441,832	284,642	•	-	-	
		an a mangapetininga sakup 1111				the right of the constitution of	- Tabada (1940) - 1944	>	ĺ

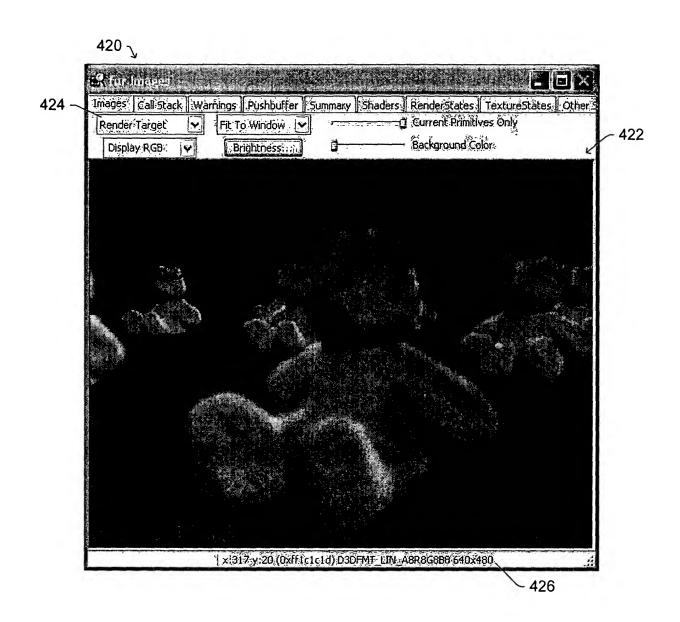


Fig. 10

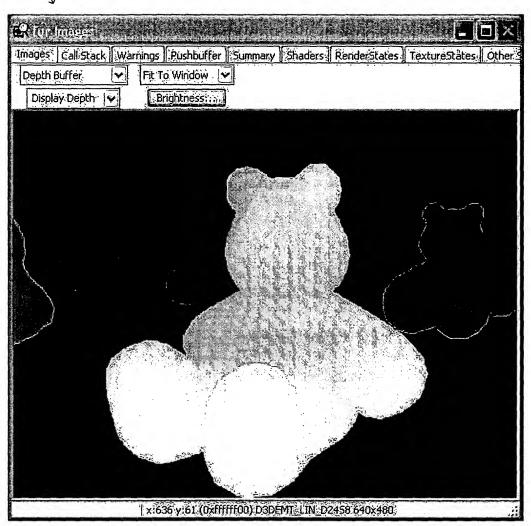
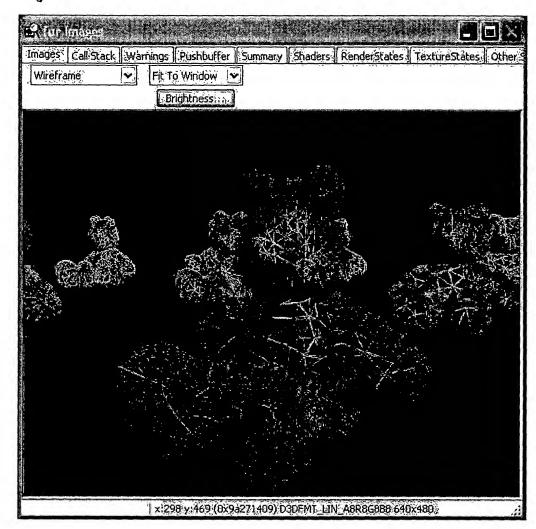


Fig. 11



420 _\ 🥝 fur Images Images Call Stack Warnings Pushbuffer Summary Shaders RenderStates TextureStates Other Fit To Window All Textures 434 428 430 432 No Texture

Fig. 13



Fig. 14

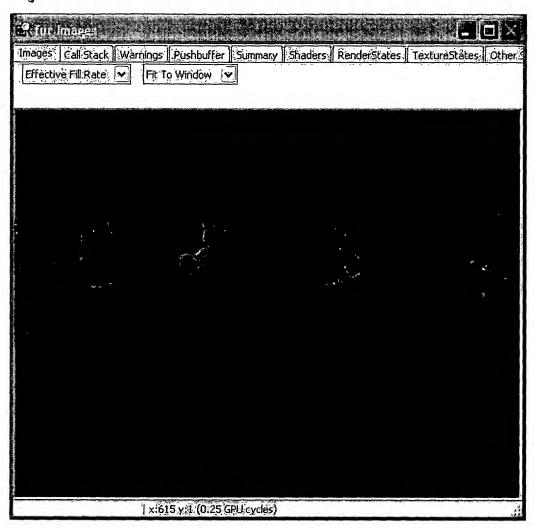


Fig. 15

Path to Symbol 1	ile: c:\xboxbins\dump		Browse ¹ Resolve Symbols)
Event "	Symbol	Line	Filé
BlockOnObject	D3D::BlockOnTime D3D::BlockOnNonSurfaceResource D3DFixup_Reset; CXBoxSample::FrameMove: CXBApplication::Run main mainXapStartup	537- 1287 1857 363 294 108	c.\xbox\private\windows\directx\dxg\d3d8\se\pusher.cpp c:\xbox\private\windows\directx\dxg\d3d8\se\pusher.cpp c:\xbox\private\windows\directx\dxg\d3d8\se\pushres.cpp c:\xbox\private\atg\samples\graphics\pushbuffer.cp c:\xbox\private\atg\samples\common\src\xbapp.cpp c:\xbox\private\atg\samples\graphics\pushbuffer.cp c:\xbox\private\atg\samples\graphics\pushbuffer.cp
Cleara.	D3DDevice_Clear CXBoxSample: Render main: main:XapiStartup	74 383 108 54	c:\xbox\private\windows\directx\dxg\d3d8\se\clear.cpp c:\xbox\private\atg\samples\graphics\pushbuffer.cp c:\xbox\private\atg\samples\graphics\pushbuffer.cp c:\xbox\private\ntos\xapi\dl\xapi0.c
RunPushBuller	D3DDevice_RumPushBuffer CXBoxSample::Render main mainXapiStartup	122 386 108 54	c:\xbox\private\windows\directx\dxg\d3d3\se\pushres.cpp c:\xbox\private\atg\samples\graphics\pushbuffer\pushbuffer.cp c:\xbox\private\atg\samples\graphics\pushbuffer\pushbuffer.cp c:\xbox\private\ntos\xapr\df\xapi0.c
DrawVerticesUP			
Begin/End	D3DDevice_Begin CXBFont:Begin CXBoxSample::Render main mainXapiStartup	1195 448 387 108	c:\xbox\private\windows\directx\dxg\d3d8\se\diawpiiin.cpp c:\xbox\private\atg\samples\common\src\xbfont.cpp c:\xbox\private\atg\samples\graphics\pushbuffer.cp c:\xbox\private\atg\samples\graphics\pushbuffer\pushbuffer.cp c:\xbox\private\atg\samples\graphics\pushbuffer\pushbuffer.cp

Fig. 16

460 _{\(\sqrt{}\)} 462 📍 fur Warnings Images | Call Stack | Warnings | Pushbuffer | Summary | Shaders | RenderStates | TextureStates | Other State ✓ Display Priority 2 Warnings ✓ Display Priority 1 Warnings ✓ Display Priority 3 Warnings Priority | Message ID | Event Clear If all redundant state setting were perfectly eliminated, rendering of entire scene would be 0. 3 2 The CPU's floating point precision is set to 53 bits. Consider calling controlfof PC 24, MC 3 Begin/End Vertex shader is writing to 9 output registers that are unused by the current pixel shader. 3 To make best use of pixel pipelines and swathing, use a single clipped triangle that covers the 74 3 DrawIndexedVertices Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 106 DrawIndexedVertices 3 Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 138 DrawIndexedVertices Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 173 3 DrawIndexedVertices Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 206 DrawIndexedVertices 3 Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 210 3 DrawIndexedVertices Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 243 **DrawIndexedVertices** 3 Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 247 **DrawIndexedVertices** 3 Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 280 DrawIndexedVertices Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 282 DrawIndexedVertices 3 Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 284 DrawIndexedVertices 3 Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 288 DrawIndexedVertices Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 321 DrawIndexedVertices 3 Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 325 **DrawIndexedVertices** 3 Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 329 **DrawIndexedVertices** 3 Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 333 DrawIndexedVertices 3 Vertex shader is writing to 1 output registers that are unused by the current pixel shader. 2 336 D3DPRESENT_INTERVAL_ONE_OR_IMMEDIATE and D3DPRESENT_INTERVAL_TWO Begin/End < Ш

Event	ՙբնցիկ՚՚ֈֈֈ֎՟	Size	Attributes	
BlockOnObject				
Clear	Clear D3DCLEAR_TARGET ID3DCLEAR_ZBUFFER ID3DCLEAR_STENCIE)	28		
RunPushBuffer				
DrawVerticesUP	D3DRS_PSCOMBINERCOUNT	8	Redundant	
	D3DRS_PSRGB(NPUTS*	36	Redundant.	
	D3DRS_FSRGBOUTPUTS*	36	Redundant	
M. A.	D3DAS_PSALPHAINPUTS*:	36	Redundant.	
	D3DRS_PSALPHAQUIPUTS*	36	Redundant	
	LazySetShaderStageProgram	8	Redundant.	
	SelVertexShaderConstant	44		
	SetVertexShader/SelectVertexShader	208		
	LazySetSpecFogCombiner	.8	Redundant	
	D3DRS_ESFINALCOMBINERINPUTSABCD	8		
	D3DRS_PSFINALCOMBINERINPUTSEFG	4		
	LazyŚetState/SetVertexShaderInput	100		
	Jump "	4		
	D3DRS_CULLMODE	.8		
	D3DRS_ALPHABLENDENABLE	532		
	SetVertexShaderConstant	76		
	SetVertexShader/SelectVertexShader	136		
	CommonSetViewport	52	Redundant,	
	SetVertexShader/SelectVertexShader	8	Redundant	
	D3DÁS_PSCÔMBINEACOUNT	8		
	D3DRS_RSRGBINPUTS*	36	***************************************	
	D3DRS_PSRGBOUTPUTS*	36 36		
	D3DRS PSALPHAINPUTS*	36		

468 _{\(\sqrt{}\)}

Summary Timing Data Summary Total CPU Time Total GPU Time Approximate Framerate Display Format D3DFMT_LIN_A8886888	Value 11,437,802 ns 11,280,032 ns 87,43 [ps	in Annial Control of the Control of	ه و د دون ورتان ها دو الاستفاد و برود	<u> </u>
Total CPU Time Total GPU Time Approximate Framerate Display Format	11,280,032 ns			
Total GPU:Time Approximate Framerate Display Format	11,280,032 ns			A STATE OF THE STA
Display Format	87:43 fps			
				and the contract of the contra
D3DFMT_LIN_A8R8G8B8				· · · · · · · · · · · · · · · · · · ·
•	640 x 480			
State changes				
Textures	67 54			
Vertex buffers	54			
Palettes	'0 (~ .			
Color buffers	7			
Z buffers	.0			
Vertex shader programs	. 20			
Vertex shader constants	145			, 🖹
Fences	13			
KickOffs	13 24 0			
Jumps:	.0			
Vertex data types				
D3DVSDT_FLOAT2	101			
D3DVSDT_FLOAT3:	115			
D3DVSDT_D3DCOLOR	1			

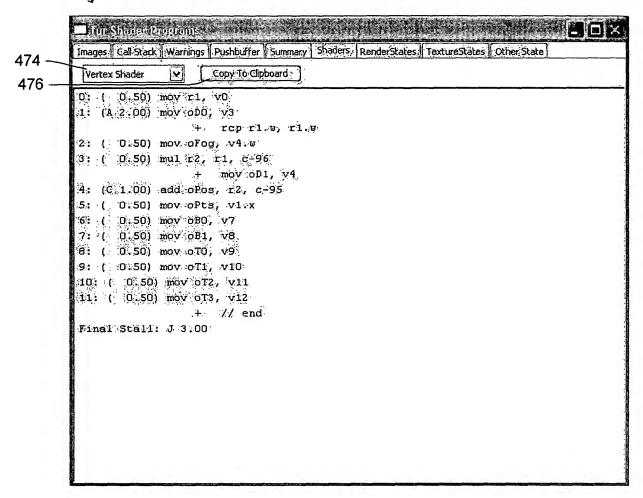


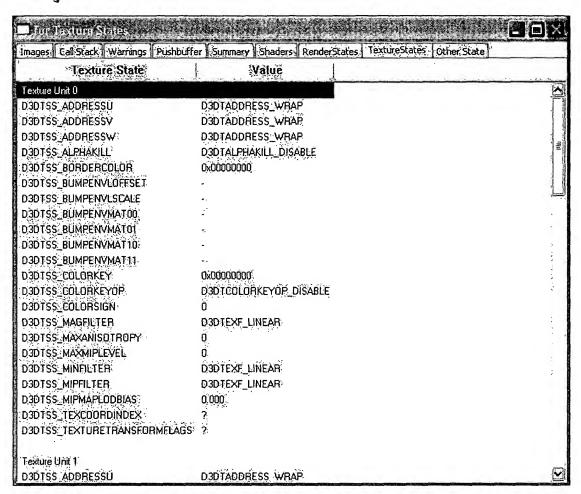
Fig. 20

480 _{\(\sqrt{}\)}

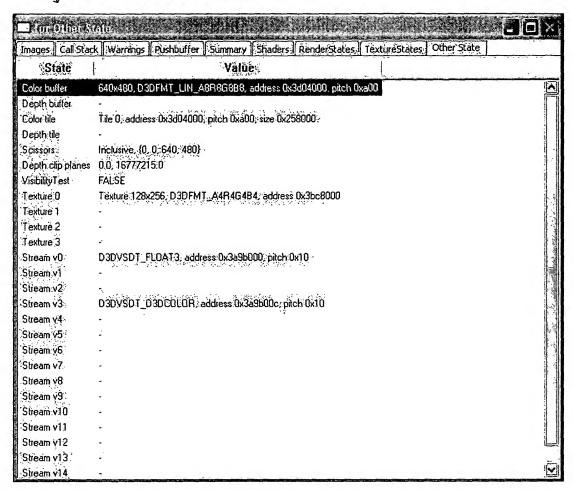
fur KenderStates	美国的代表的1971年的第二人 的第三人称单数	801
Images Call Stack Warnings Pushbuffer	Summary Shaders RenderStates TextureStates Other	State
RénderState	<u>Value</u> :	
D3DRS_ALPHABLENDENABLE	TRUE	2
D3DRS_ALPHAFUNC	D3DCMP GREATEREQUAL .	Ţ
D3DRS_ALPHAREF	::0x08	¥ 1
D3DRS_ALPHATESTENABLE	TRUE.	
D3DRS_BACKFILLMODE	D3DFILL_SOLID	1
D3DRS_BLENDCOLOR	· 030000000 ·	
D3DAS_BLENDOP	D3DBLENDOP_ADD	
D3DRS_COLORWRITEENABLE	D3DCOLORWRITEENABLE_ALL	1
D3ORS_CULLMODE	D3DCULL_CCW	<u>;</u>
D3DHS_DEPTHCLIRCONTROL	D3DDCC_CULLPRIMITIVE	;
D3DRS_DESTBLEND	D3DBLEND_INVSRCALPHA	Ž.
D3DRS_DITHERENABLE	EALSE	<u>.</u>
D3DRS_DONOTCULLUNCOMPRESSED	FALSE	
D3DRS_DXT1NOISEENABLE	FALSE	· y
D3DRS_EDGEANTIALIAS	FALSE	<u> </u>
D3DRS_FILLMODE	,D3DFILL_SOLID	į
D3DRS_FOGCOLOR	0,000,000,00	4
D3DRS_FOGDENSITY	?	÷
D3DRS_FOGENABLE	FALSE	;
D3DRS_FOGEND	? ·	:
D3DRS_FOGSTART	· ?	
D3DRS_FOGTABLEMODE	D3DFOG_NONE	
D3DRS_FRONTFACE	D3DFRONT_CW	
D3DRS_LIGHTING	FALSE	
D3DRS_LINEWIDTH	1,000	
D3DRS LOCALVIEWER	FALSE	

Inventor(s): Kyle R. Johns and J. Andrew Goossen Title: Facilitating Performance Analysis for Processing

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488 _{\(\sqrt{}\)}



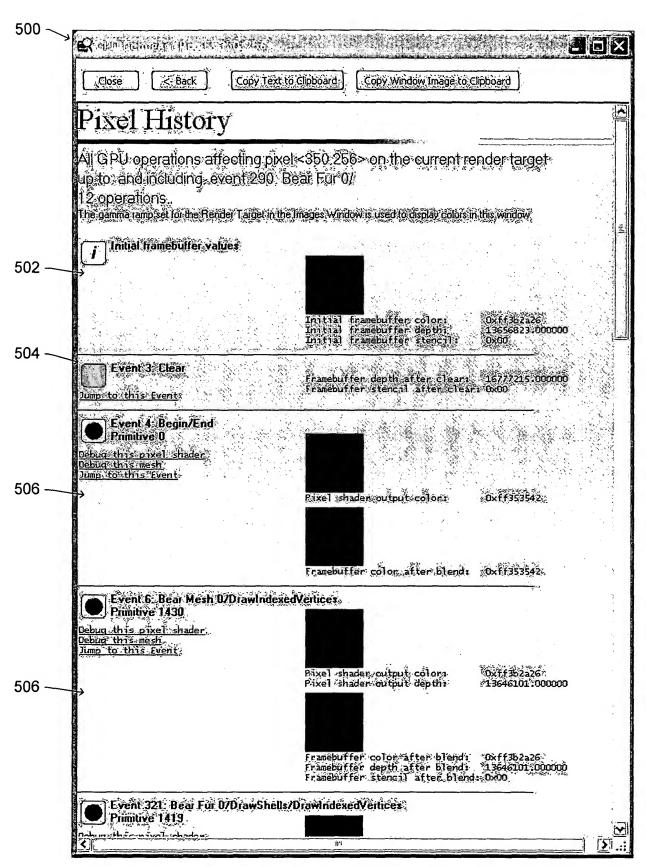


Fig. 24

Copy Text to Clipboard Copy Window Image to Clipboard Pixel Shader Debugger Pixel<350,256> Event 4 Begin/End The gamma ramp set for the Pender Target in the Images Window is used to display colors in this window. Color Combiner 0 mov:ro.rqb/yo_sat.rqb + mov r0 a, v0_sat a Inputs: v0: 0x0ff 0x035 0x035 0x042 Outputs: r0: 0x0ff 0x035 0x035 0x042 Final Combiner xfc zero_sat rgb, zero_sat rgb, zero_sat rqb, r0_sat rgb, zero_sat rqb, zero r0: 0x0ff 0x035 0x035 0x042 Outputs: Out: 0xff 0x35 0x35 0x42 This pixel was rendered using a vertex shader program from the following primitive with 3 vertices: 522 -

	Sha	der D	ebûgger		***************************************					
			Cougger							
ent 4: Be rtex0	idili/Filia		•							
instruction	ons:								-	
f annually specially special	troffice and the first constant and	Outp	uts					nputs	•	
Reg	X		, Z:	, u }	Reg	X X		AF.	2	
mov rl	ÓŶ.				, ·		,	1 4.		
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mov o D	0. v3 +	rcp rl	w ri w							
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mul r2,	r1. c-96	∰ môy	oD1, y4		1 4.	,"4		9 8 8 E	*	
r2: oD1:	-0.5 0	-0.5 1.67	7772e+007:	1015-020	rı:	-0.5	-0.5		1.	Ĭ.
L			,0,0,,42,		V4:	<u>.</u> .	0	∴ 67772e+00	0,5.42101	1 le-020
add oP	os, r2, c	⊵9 5								
oPos:	0	0.1.67	772e+007	ii.	r2: c-95:	-0.5	0.5 1	.67772e+00	i Ž	1.
<u> </u>					U#33:	Uya	0.5	*	0.5:42101	.e. . 020}
mov oP	ls, vi x									
oPts:	-0.25 -	0.25 -	0:304635: -0	0:304635	vi:	÷0°, 25×⇒	0.1875	-0.9284	9.	1 ,
) 						_			_	
mov oB	0. y7						-		-	
ово:	Augmann Ayamara Maran	17	1.	÷1-₹	V7:	1	1			

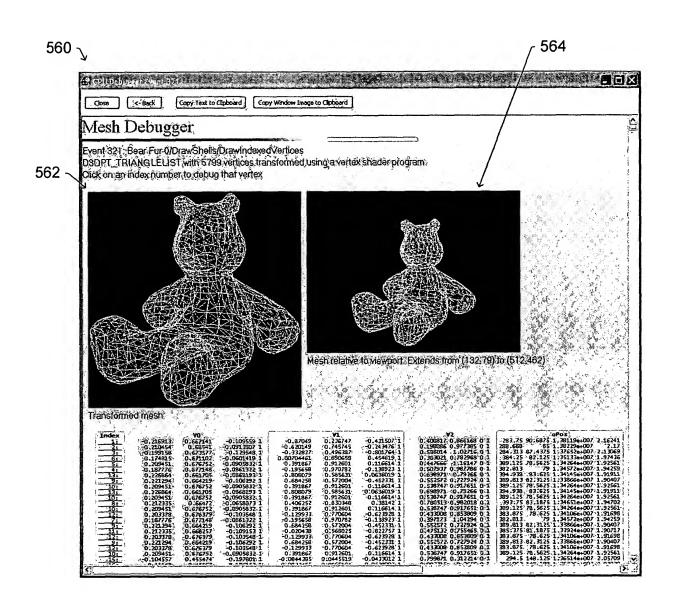


Fig. 27

